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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/675,529	09/29/2000	James F. Frye	1662-30200 (P99-2848)	1581	
22879	7590 05/03/2004	EXAMINER			
	PACKARD COMPANY	KISS, ERIC B			
P O BOX 272400, 3404 E. HARMONY ROAD INTELLECTUAL PROPERTY ADMINISTRATION			ART UNIT	PAPER NUMBER	
FORT COLL	LINS, CO 80527-2400	2122	15		
•			DATE MAILED: 05/03/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary		Applicatio	n No.	Applicant(s)	In		
		09/675,529	9	FRYE ET AL.			
		Examiner		Art Unit			
		Eric B. Kiss		2122			
Period fo	The MAILING DATE of this commun or Reply	ication appears on the	cover sheet with the o	correspondence addres	SS		
THE - Exte after - If the - If NO - Failt Any	ORTENED STATUTORY PERIOD F MAILING DATE OF THIS COMMUN nsions of time may be available under the provisions SIX (6) MONTHS from the mailing date of this comr period for reply specified above is less than thirty (3) period for reply is specified above, the maximum st ure to reply within the set or extended period for reply reply received by the Office later than three months ed patent term adjustment. See 37 CFR 1.704(b).	ICATION. of 37 CFR 1.136(a). In no ever nunication. O) days, a reply within the statur atutory period will apply and will will, by statute, cause the appli	nt, however, may a reply be tir tory minimum of thirty (30) day expire SIX (6) MONTHS from cation to become ABANDONE	mely filed  ys will be considered timely.  n the mailing date of this commu ED (35 U.S.C. § 133).	unication.		
Status							
1)⊠	Responsive to communication(s) file	ed on 12 November 20	003.				
2a)□							
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
5)⊠ 6)⊠ 7)⊠	Claim(s) <u>2-14</u> is/are pending in the application.  4a) Of the above claim(s) is/are withdrawn from consideration.  Claim(s) <u>13 and 14</u> is/are allowed.  Claim(s) <u>2-8 and 12</u> is/are rejected.  Claim(s) <u>9-11</u> is/are objected to.  Claim(s) are subject to restriction and/or election requirement.						
Applicat	ion Papers						
10)⊠	The specification is objected to by the The drawing(s) filed on <u>01 April 200</u> Applicant may not request that any objected that any objected that on declaration is objected that on the specific transfer of trans	$\underline{2}$ is/are: a) $\square$ accepte ection to the drawing(s) bg the correction is require	e held in abeyance. Seed if the drawing(s) is ol	ee 37 CFR 1.85(a). bjected to. See 37 CFR 1			
Priority	under 35 U.S.C. § 119						
a)	Acknowledgment is made of a claim  All b) Some * c) None of:  1. Certified copies of the priority  2. Certified copies of the priority  3. Copies of the certified copies application from the Internation  See the attached detailed Office action	documents have been documents have been of the priority documents have been of the priority documents and Bureau (PCT Rule	n received. n received in Applica ents have been receiv e 17.2(a)).	tion No ved in this National Sta	age		
2) Noti 3) Info	nt(s) ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review ( rmation Disclosure Statement(s) (PTO-1449 o er No(s)/Mail Date		4) Interview Summar Paper No(s)/Mail [ 5) Notice of Informal 6) Other:		52)		

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#### **DETAILED ACTION**

1. The reply filed November 12, 2003, has been received and entered. Claims 2-14 are pending.

## Response to Amendment

- 2. Applicant's amendment to the specification appropriately addresses the objection to the drawings as detailed in the previous office action. Accordingly, this objection is withdrawn in view of Applicant's amendment.
- 3. Applicant's amendment to the abstract appropriately addresses the objection to the specification as detailed in the previous office action. Accordingly, this objection is withdrawn in view of Applicant's amendment.

### Allowable Subject Matter

- 4. Claims 13 and 14 are allowed.
- 5. The indicated allowability of claims 2-12 is withdrawn in view of the newly discovered reference(s), rejections based on which follow.

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6. Claims 9-11 are objected to as being dependent upon a rejected base claim (see the rejection of claim 8 below, but would be allowable if rewritten in independent form including all

of the limitations of the base claim and any intervening claims.

Claim Rejections - 35 USC § 102

7. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the

basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on

sale in this country, more than one year prior to the date of application for patent in the United States.

8. Claims 2-8 and 12 are rejected under 35 U.S.C. 102(b) as being anticipated by "Plug and

Play BIOS Specification," 1994, Version 1.0 A (hereinafter PnPBIOS).

As per claim 2, *PnPBIOS* discloses a processor (inherent in executing the BIOS

software); at least one input/output device coupled to said processor (a BIOS); a flashable ROM

device coupled to said processor and containing a configuration table (see, for example, sections

2.1.1, 4.1, and 4.2); a non-volatile, non-flashable memory device coupled to said processor and

containing an extension configuration table (Extended System Configuration Data stored in

NVRAM; see, for example, subsection 4.7); wherein configuration information can be added to

the computer system by storing such configuration information in the non-volatile, non-flashable

memory device (see, for example, section 4.7); and wherein, upon executing a set-up routine to

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configure the computer system for a newly installed device, said processor examined the flashable ROM device to determine whether configuration information pertaining to the newly installed device is present and, if not, said processor the examines the non-volatile, non-flashable memory device to determine whether the configuration information pertaining the newly installed device is present (*PnPBIOS* discloses a BIOS POST sequence in which constructs a resource map by first processing devices with statically allocated resources (see, for example, section 2.5, enumerated step 3 on pages 13-14) and subsequently performing any appropriate dynamic resource allocations (see, for example, section 2.5, enumerated step 7 on page 15)).

As per claim 3, *PnPBIOS* further discloses the non-volatile, non-flashable memory device comprising a non-volatile RAM device (see, for example, section 4.7).

As per claims 4-7, *PnPBIOS* further discloses the configuration tables containing option identifiers/board identifiers and corresponding configuration data (see, for example, sections 4.1, 4.2, and 4.7).

As per claim 8, *PnPBIOS* discloses adding a device to a computer (see, for example, section 4.7); first searching a ROM memory in which configuration information is stored for the configuration information pertaining to the added device and then, if the configuration is not found in the ROM memory, searching a non-volatile RAM memory for the configuration information (*PnPBIOS* discloses a BIOS POST sequence in which constructs a resource map by first processing devices with statically allocated resources (see, for example, section 2.5, enumerated step 3 on pages 13-14) and subsequently performing any appropriate dynamic resource allocations (see, for example, section 2.5, enumerated step 7 on page 15)). *PnPBIOS* discloses a ROM device (Plug and Play BIOS) coupled to said processor and containing a

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configuration table (system device configuration list consisting of system device nodes that maintain resource data for systemboard devices; see, for example, subsections 4.1 and 4.2). PnPBIOS discloses a non-volatile, non-flashable memory device coupled to said processor and containing an extension configuration table (Extended System Configuration Data stored in NVRAM; see, for example, subsection 4.7)

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As per claim 12, *PnPBIOS* discloses storing configuration information in the non-volatile RAM memory when a device is added to the computer (see, for example, section 4.7).

#### Conclusion

9. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Eric B. Kiss whose telephone number is (703) 305-7737. The Examiner can normally be reached on Tue. - Fri., 7:30 am - 5:00 pm. The Examiner can also be reached on alternate Mondays.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Tuan Dam, can be reached on (703) 305-4552. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

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EBK / EBK April 29, 2004

WEI Y. ZHEN
PRIMARY PATENT EXAMINER